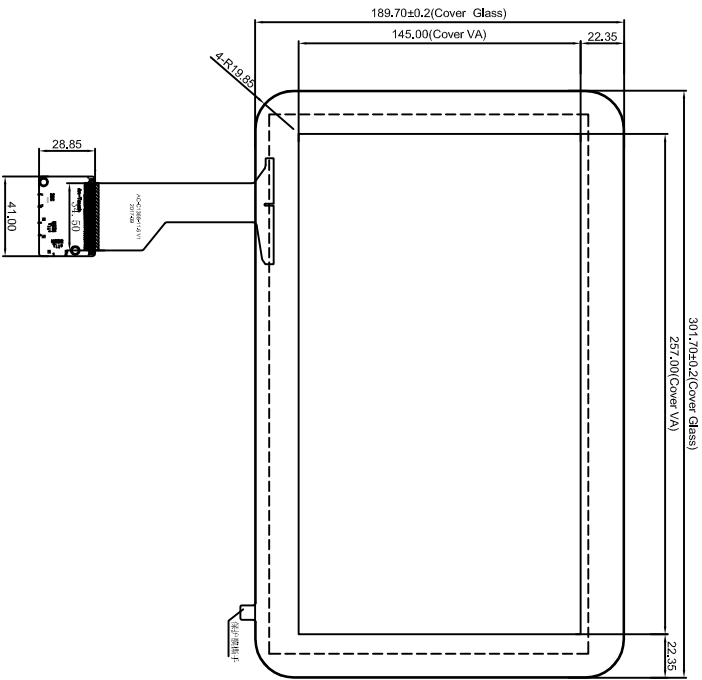
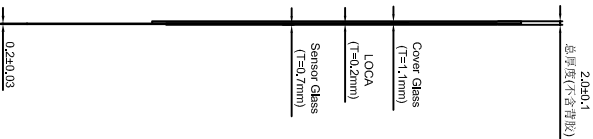


REV	MARK	HISTORY	DATE	NAME
REV.0	—	—	2016.11.04	JSY

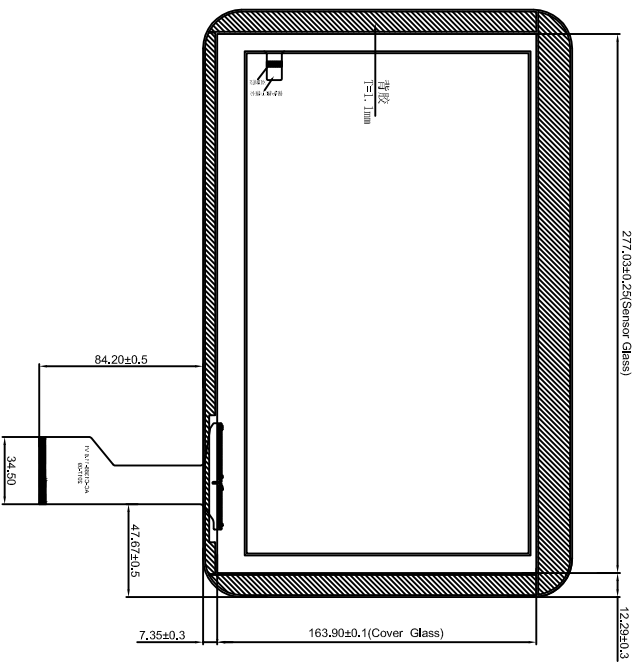
Front View



Side View



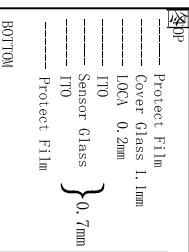
Back View



Notes:

1. TP Structure: G+G;
2. TP Type: COB; IC: ILITEK2511
3. Transmittance:  $\geq 85\%$
4. Hardless:  $> 6H$
5. Operating conditions:  $-10^{\circ}\text{C} \sim +60^{\circ}\text{C}$ ,  $\leq 85\% \text{RH}$
6. Storage conditions:  $-10^{\circ}\text{C} \sim +65^{\circ}\text{C}$ ,  $\leq 85\% \text{RH}$
7. Unspecified tolerance:  $\pm 0.2 \text{mm}$
8. All materials in the drawing comply with the RoSh

TP 叠构



AC-C1253-11.6-B Drawing

DESIGN	DATE	SHEET	UNIT	SCALE	1st ANGLE PROJECTION
CHECK	DATE	1 of 1	mm	1 : 1	
JSY	2017.04.22				

J1 (USB通讯)

PIN	定义	PIN	定义
1	3.3V	2	3.3V
2	VDD/5V	3	3.3V
3	D-	4	D+
4	D+	5	GND
5	GND	6	RST

J2 (I2C通讯)

PIN	定义
1	3.3V
2	3.3V
3	D+
4	D-
5	GND
6	RST