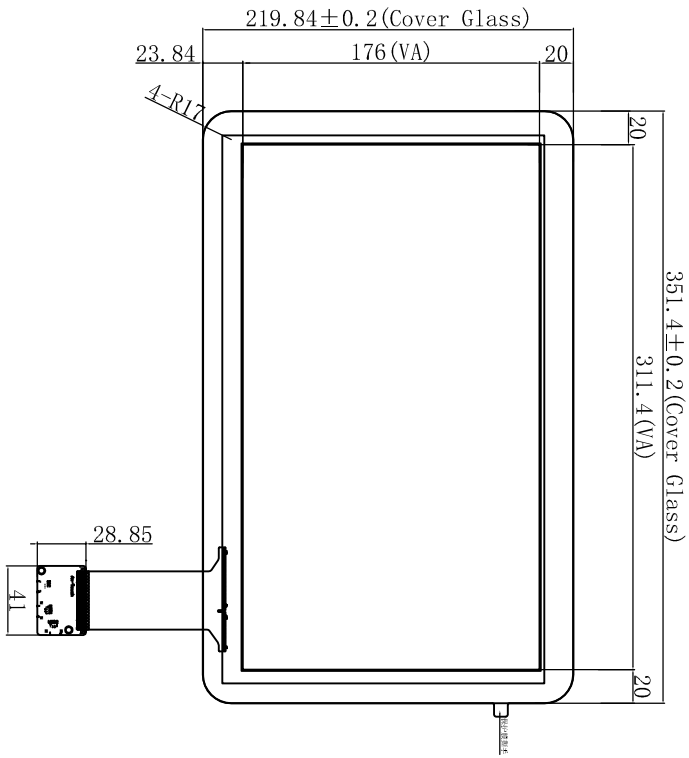
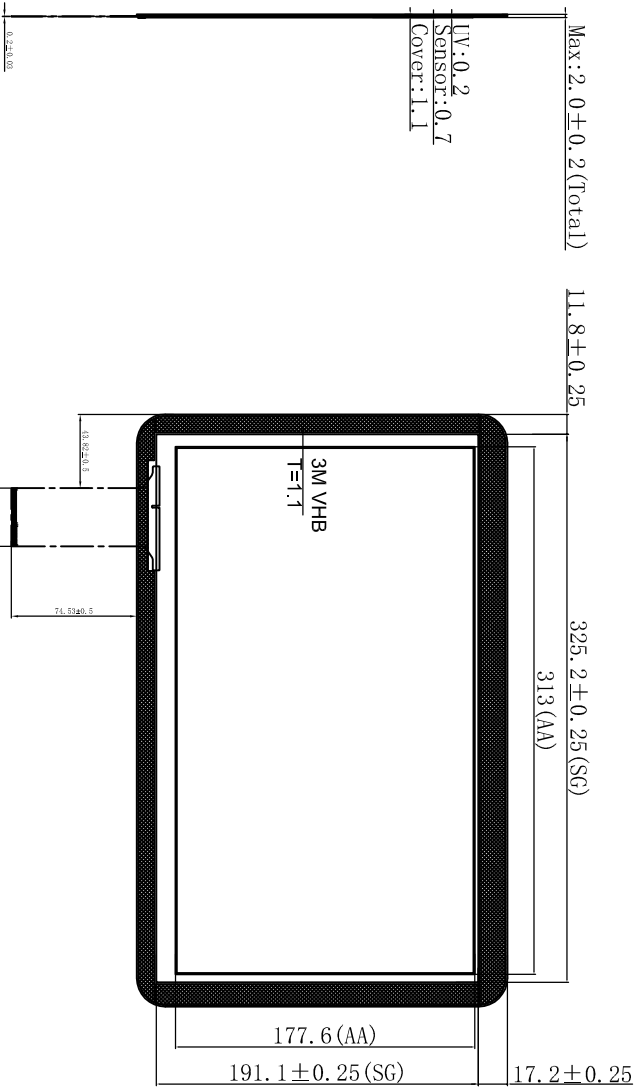


REV	MARK	HISTORY	DATE	NAME
REV. 0	—	—	2015. 11. 20	JSY

TOP VIEW



SIDE VIEW



BACK VIEW

- Notes:
1. TP Structure: G+G;
  2. TP Type: COB; IC: ILITEK2511
  3. Transmittance:  $\geq 85\%$
  4. Hardless:  $> 6H$
  5. Operating conditions:  $-10^{\circ}\text{C}^{\sim}+60^{\circ}\text{C}$ ,  $\leq 85\%RH$
  6. Storage conditions:  $-10^{\circ}\text{C}^{\sim}+65^{\circ}\text{C}$ ,  $\leq 85\%RH$
  7. Unspecified tolerance:  $\pm 0.2\text{mm}$
  8. All materials in the drawing comply with the Rosh

J1 (USB通讯)

PIN	定义
1	VDD/5V
2	D-
3	D+
4	GND

AC-C1254-14.0 (USB) DRAWING				SHEET	1 of 1	UNIT	mm	SCALE	1 : 1	1st ANGLE PROJECTION
DESIGN	JSY	DATE	2017.04.03							
CHECK	JSY	DATE	2017.04.03							